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PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Yoshio Dejima
Serial No.: 10/795,787
Conf. No.: 4078
Filed: March 8, 2004
For: METHOD FOR FABRICATING A
THIN FILM TRANSISTOR USING A
HALF-TONE MASK
Art Unit: 2822
Examiner: Duong, Khanh B.
Patent No.: 7,259,045
Issued: August 21, 2007

I hereby certify that this paper is being deposited with the United States Postal Service as FIRST-CLASS mail in an envelope addressed to: Attn: Certificate of Corrections Branch, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this date.

February 7, 2008
Date

Attorney for Applicant(s)
Registration No. 29,367

REQUEST FOR CERTIFICATE OF CORRECTION
UNDER RULE 322

Attention Certificates of Correction Branch
Commissioner for Patents
PO Box 1450
Alexandria, VA 22313.1450

Certificate
FEB 15 2008
of Correction

Dear Sir:

In accordance with 37 C.F.R. § 1.322, patentees, through their attorneys, respectfully request that a Certificate of Correction be issued in the above-referenced patent.

The error occurred as a result of mistake on the part of the Patent and Trademark Office and the change includes the following:

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Patent Publication
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IN THE ISSUE PATENT:

In the Claims:

Column 15, line 20, before the word "method" please insert --The-- (as correctly indicated in Amendment B, page 5, line 1).

A Certificate of Correction incorporating the delineated change is enclosed in duplicate herewith. Since the mistake was on the part of the Patent and Trademark Office, a Certificate of Correction should be issued without expense to the patentee and such is respectfully requested.

Respectfully submitted,

GREER, BURNS & CRAIN, LTD.

By



Patrick G. Burns
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February 7, 2008

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APPLICATION NO.: 10/795,787

ISSUE DATE : August 21, 2007

INVENTOR(S) : Yoshio Dejima

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MAILING ADDRESS OF SENDER (Please do not use customer number below):

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FEB 15 2008

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